

**IN THE SPECIFICATION:**

Please replace the FIRST full paragraph of specification page 1, lines 2-6 with the following replacement paragraph:

—

The present invention is related to the following co-pending and commonly assigned U.S. Patent Application Serial No. 10/035,603 filed on 12/28/2001 (~~112056-0036~~) titled, *Correcting Multiple Block Data Loss in a Storage Array Using a Combination of a Single Diagonal Parity Group and Multiple Row Parity Groups*, which was filed on even date herewith and which application is hereby incorporated by reference as though fully set forth herein.

—